POSSIBLE SCHEDULE FOR ECE AY2014 INTAKE STUDENTS

Possible Schedule (3 years) for Direct Entry (Poly) Students admitted to EE2 in Sem 1, AY2014/15 (Student not taking VIP or EE3032)

Schedule III	Schedule IV	Schedule V	Schedule VI	Schedule VII	Schedule VIII
MA1301[1] Introductory Mathematics	MA1505 Maths I	MA1506 Maths II	EE2012 Analytical Methods in ECE	EE4001 BEng Dissertation	EE4001 BEng Dissertation
CS1010E Programming Methodology	PC1222 Fundamentals of Physics II	EE2011 Engineering Electromagnetics	PC2232 Physics for EE [Pre-Requsite:EE2011]	EG2401 Engineering Professionalism	DEPTH ELECTIVE
EE1001 Emerging Technologies in EE	EE2021 Devices & Circuits	EE2023 Signals & Systems	EE2032 Signals & Communications Lab (3MCs)	ELECTIVE (OUTERCORE)	DEPTH ELECTIVE
EE1003 Intro to Signals & Communications	EE2024 Programming for Computer Interfaces [Pre-Requsite:EE2020] (5MCs)	EE2025 Power Electronics	EE3031 Innovation & Enterprise Project I	ELECTIVE (OUTERCORE)	1 Unrestricted Module (UEM)
EE2020 Digital Fundamentals (5MCs)	GEM Module: **GEK1549 Critical Thinking & Writing	EE2031 Circuits & Systems Design Lab (3MCs)	1 ULR-Breadth	ELECTIVE (OUTERCORE)	DEPTH ELECTIVE
*ES1102 (those not exempted)		1 SS Module	BREADTH / DEPTH ELECTIVE (UNRESTRICTED)		
21 MC	**21 MC**	**23 MC**	**23 MC**	**21 MC**	**22MCs**

IMPORTANT:

1. MA1301 & PC1222 are taken as compulsory program requirements. **Students exempted from MA1301** will take MA1505 in the first semester & will need to **take one additional Technical Elective to make-up the 4MCs shortfall.**

2. AY1415 Poly student will take **Bridging modules (MA1301, PC1222) & 1 Technical Elective/VIP/EE3032** in lieu of Industrial Attachment (under Program Requirements).

3. Students are required to take **7 Technical Electives** if not taking VIP or EE3032.

4. *ES1102 (English Module)– For students who are not exempted. Refer to http://www.eng.nus.edu.sg/ugrad/welcome/get.html for more details.

5. **GEK1549 is a compulsory English module for B.Eng(EE) students admitted from AY2014/15. The module should be read to fulfill **GEM A requirements**.

6. The above University Level Requirements (UEM/ULR/SS) are for reference only. Students are free to re-schedule these modules as they are done by biding via CORS.

7. The technical electives satisfying the EE Outercore/Breadth/Depth requirements can be taken at any semester upon satisfying the pre-requisites.

Possible Schedule (3 years) for Direct Entry (Poly) Students admitted to EE2 in Sem 1, AY2014/15 (with VIP or EE3032)

Schedule III	Schedule IV	Special Term (ST)-*Optional*	Schedule V	Schedule VI	Schedule VII	Schedule VIII
MA1301[1] Introductory Mathematics	MA1505 Maths I	(EE3032, 6MCs as Technical Elective) OR (VIP, 6MCs) – Option[3]	MA1506 Maths II	EE2012 Analytical Methods in ECE	EE4001 BEng Dissertation	EE4001 BEng Dissertation
CS1010E Programming Methodology	PC1222 Fundamentals of Physics II		EE2011 Engineering Electromagnetics	PC2232 Physics for EE [Pre-Requisite:EE2011]	EG2401 Engineering Professionalism	DEPTH ELECTIVE
EE1001 Emerging Technologies in EE	EE2021 Devices & Circuits		EE2023 Signals & Systems	EE2032 Signals & Communications Lab (3MCs)	ELECTIVE (OUTERCORE)	DEPTH ELECTIVE
EE1003 Intro to Signals & Communications	EE2024 Programming for Computer Interfaces [Pre-Requsite:EE2020] (5MCs)		EE2025 Power Electronics	EE3031 Innovation & Enterprise Project I	ELECTIVE (OUTERCORE)	1 Unrestricted Module (UEM)
EE2020 Digital Fundamentals (5MCs)	GEM Module: **GEK1549 Critical Thinking & Writing		EE2031 Circuits & Systems Design Lab (3MCs)	1 ULR-Breadth	BREADTH / DEPTH ELECTIVE (UNRESTRICTED)	
*ES1102 (those not exempted)			1 SS Module			
21 MC	21 MC	6 MCs	23 MC	19 MC	21 MC	18 MCs

IMPORTANT:

1. MA1301 & PC1222 are taken as compulsory program requirements. **Students exempted from MA1301** will take MA1505 in the first semester & will need to **take one additional Technical Elective to make-up the 4MCs shortfall.**

2. AY1415 Poly student will take **Bridging modules (MA1301, PC1222) & 1 Technical Elective/VIP** in lieu of Industrial Attachment (under Program Requirements).

3. Student can opt to take **EE3032 (6MCs) OR VIP** (6MCs) in the Special Term. VIP can also be done in the AY1516 Special Term. Students who took EE3032 or VIP will need to take **5 Technical Electives** to fulfill their graduation requirements.

4. *ES1102 (English Module)– For students who are not exempted. Refer to http://www.eng.nus.edu.sg/ugrad/welcome/get.html for more details.

5. **GEK1549 is a compulsory English module for B.Eng(EE) students admitted from AY2014/15. The module should be read to fulfill **GEM A requirements**.

6. The above University Level Requirements (UEM/ULR/SS) are for reference only. Students are free to re-schedule these modules as they are done by biding via CORS.

7. The technical electives satisfying the EE Outercore/Breadth/Depth requirements can be taken at any semester upon satisfying the pre-requisites.

8. Accelerating students can take EE4001 when they achieve level 4 standing.

Possible Schedule (3 years) for Direct Entry (Poly) Students admitted to EE2 in Sem 1, AY2014/15 (with VIP and EE3032)

Schedule III	Schedule IV	Special Term (AY1415)	Schedule V	Schedule VI	Special Term (AY1516)	Schedule VII	Schedule VIII
MA1301[1] Introductory Mathematics	MA1505 Maths I	EE3032[3] Innovation & Enterprise Project II (optional) - Pre-req EE2024	MA1506 Maths II	EE2012 Analytical Methods in ECE	Vacation Internship Programme (VIP) - 6MCs	EE4001 BEng Dissertation	EE4001 BEng Dissertation
CS1010E Programming Methodology	PC1222 Fundamentals of Physics II		EE2011 Engineering Electromagnetics	PC2232 Physics for EE [Pre-Requsite:EE2011]		EG2401 Engineering Professionalism	DEPTH ELECTIVE
EE1001 Emerging Technologies in EE	EE2021 Devices & Circuits		EE2023 Signals & Systems	EE2032 Signals & Communications Lab (3MCs)		ELECTIVE (OUTERCORE)	DEPTH ELECTIVE
EE1003 Intro to Signals & Communications	EE2024 Programming for Computer Interfaces [Pre-Requsite:EE2020] (5MCs)		EE2025 Power Electronics	EE3031 Innovation & Enterprise Project I		ELECTIVE (OUTERCORE)	1 Unrestricted Module (UEM)
EE2020 Digital Fundamentals (5MCs)	GEM Module: **GEK1549 Critical Thinking & Writing		EE2031 Circuits & Systems Design Lab (3MCs)	1 ULR-Breadth			
*ES1102 (those not exempted)			1 SS Module				
21 MC	21 MC	6 MCs	23 MC	19 MC	6 MCs	17 MC	18 MCs

IMPORTANT:

1. MA1301 & PC1222 are taken as compulsory program requirements. **Students exempted from MA1301** will take MA1505 in the first semester & will need to **take one additional Technical Elective to make-up the 4MCs shortfall.**
2. AY1415 Poly student will take **Bridging modules (MA1301, PC1222) & 1 Technical Elective/VIP/EE3032** in lieu of Industrial Attachment (under Program Requirements).
3. Student can opt to take **EE3032** (6MCs) **AND** **VIP** (6MCs) in the Special Term. Students who took EE3032 & VIP will need to take **4 Technical Electives** to fulfill their graduation requirements.
4. ***ES1102** (English Module)– For students who are not exempted. Refer to http://www.eng.nus.edu.sg/ugrad/welcome/get.html for more details.
5. ****GEK1549** is a compulsory English module for B.Eng(EE) students admitted from AY2014/15. The module should be read to fulfill **GEM A requirements**.
6. The above University Level Requirements (UEM/ULR/SS) are for reference only. Students are free to re-schedule these modules as they are done by biding via CORS.
7. The technical electives satisfying the EE Outercore/Breadth/Depth requirements can be taken at any semester upon satisfying the pre-requisites.
8. Accelerating students can take EE4001 when they achieve level 4 standing.